

Notice of References Cited

Application/Control No.

09/714,524

Applicant(s)/Patent Under
Reexamination
CHARPENTIER ET AL.

Examiner

Brook Kebede

Art Unit

2823

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	U	Wolf et al., Silicon Processing for the VLSI era, Volume I, pp. 234-238, (1986).
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.